ARM® Cortex®-M33

32-bit Microcontroller

NuMicro® Family
M3331 Series BSP
Revision History

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Revision 3.00.002 (Released 2026-01-28)

1. Fix linker descriptor of GNU tool.
2. Add startup and system files for m3331 device and update CMakeLists for ISP and PowerManagement samples.
3. Remove header file to fix VSCode project build error in MP3_Player.
4. Update VSCode Quick Start Guide.pdf
5. Modify TrustZone sample code for FPU.
6. Remove misc controls software floating-point settings to use FPU.
7. Rename FBPCUR to FBPCODE in ELLSI driver.
8. Fix PDMA setting issue in I2S sample code.
9. Add NuVotonAudioGUI_Firmware and Codec_ControlBoard sample code to modify codec (NAU88C22) via NuVotonAudioGUI.
10. Enable Non-Secure access to Floating point Extension when FPU is enabled.
11. Revise ISP_CAN to program APROM/DF by FMC 64-bit write command.
12. Add the missing descriptions and configurations for BPWM2-5 in BPWM driver.
13. Update PDCI MFP definitions.
14. Modify name SPB to PDCI.
15. Support GCC 14.3.1 of VS Code.
16. Add PDMA1 for M3331G.
17. Fix FMC_ISPSTS_VECMAP_Msk constant definition.
18. Refactor SYS_Init function to standardize core clock setting.

Revision 3.00.001 (Released 2025-08-20)

1. Initial Release.

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